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Attorney Docket No. 048662-0129

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Lothar FANNASCH

Title: CONDUCTOR TRACK SUPPORTING LAYER FOR  
LAMINATING INSIDE A CHIP CARD, CHIP CARD  
COMPRISING A CONDUCTOR TRACK SUPPORTING  
LAYER, AND METHOD FOR PRODUCING A CHIP CARD

Appl. No.: 10/069,419 ✓

Filing Date: 04/12/2002

Examiner: Harold I. Pitts

Art Unit: 2876

**AMENDMENT AND REPLY UNDER 37 C.F.R. § 1.111**

Mail Stop NON-FEE AMENDMENT  
Commissioner for Patents  
PO Box 1450  
Alexandria, Virginia 22313-1450

Sir:

This communication is responsive to the Non-Final Office Action dated May 19, 2003, concerning the above-captioned patent application.

Amendments to the Specification begin on page 2 of this document.

Amendments to the Claims are reflected in the listing of claims which begins on page 4 of this document.

Remarks begin on page 6 of this document.

Please amend the application as follows:

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